

FIG. 1B
(PRIOR ART)



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For: SEMICONDUCTOR PACKAGES AND
METHODS FOR MAKING THE SAME

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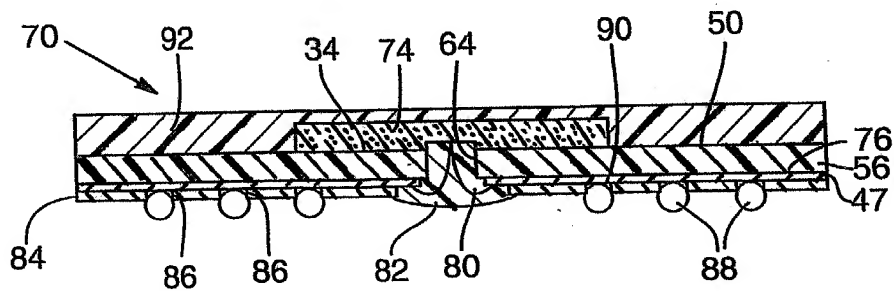


FIG. 3A

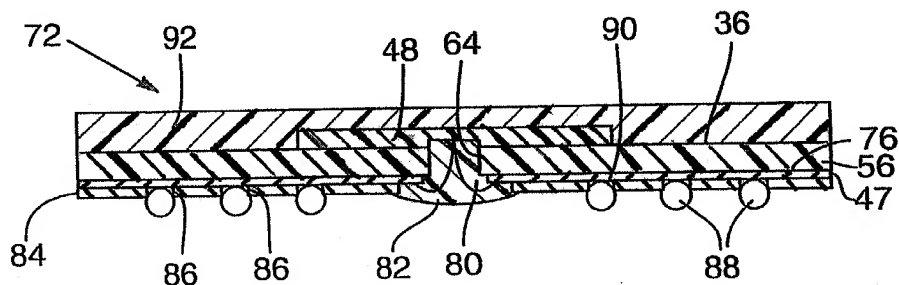


FIG. 3B